Speeding Edge

If it's current technology, it's high speed

HIGH SPEED PCB AND SYSTEM DESIGN COURSE
SAN FRANCISCO AIRPORT (MILLBRAE) CALIFORNIA

NOTE: THIS IS THE ONLY CLASS SCHEDULED FOR THE U.S. IN 2010!

Event Two-day High Speed PCB and System Design Course

Date: May 10-11, 2010

Location: Clarion Hotel San Francisco Airport
401 E. Millbrae Ave.
Millbrae, California

Course Description:

This two-day training session on the design of high-speed PCBs and their associated systems is the premier course offered by Speeding Edge and has been taught to more than 7,000 engineering professionals worldwide. Conducted by Speeding Edge owner and president, Lee Ritchey, this very practical course is thorough and comprehensive. It covers all the physics involved in high-speed design, explaining the science behind each one of the principles, their role in high-speed systems and how to properly manage the design process to account for them. This course will give the engineering professional the fundamental knowledge necessary to make the most efficient design rule set, organize the design process to efficiently execute the design, select the appropriate PCB materials and choose the toolsets that will best suit the design process. This course is especially relevant to IC designers as device speeds outstrip the traditional packages and application notes.

As speeds and complexities of ICs have grown, their power requirements have changed dramatically. The practice of "sprinkling" capacitors around a PCB has proven deficient with these ICs, leading to many design failures related to power delivery. In fact, the majority of design failures, both functional and EMI, are traceable to power delivery problems. For this reason, a major portion of this course focuses on how to design robust power delivery systems, a topic often ignored by manufacturers in their applications notes.

What's Included:

*3-ring binder containing all presentation slides used in class
*Copy of course textbooks, Right the First Time, A Practical Handbook on High Speed PCB and System Design, Volume 1 (Volume 2 is available for purchase separately).
*Breakfast, full lunch and mid-morning and afternoon refreshments, both days
*Paid parking at hotel

Cost: $1,210 per student. **REGISTRATION DEADLINE IS MAY 3, 2010!**

Accommodations: (not included in class cost)

Guest room rate is $79.00 per night single or double occupancy. Students are responsible for their own room reservations and room costs. Room reservations can be made by contacting the hotel at 650-692-6363 or through the hotel's website at www.clarionsfo.com.